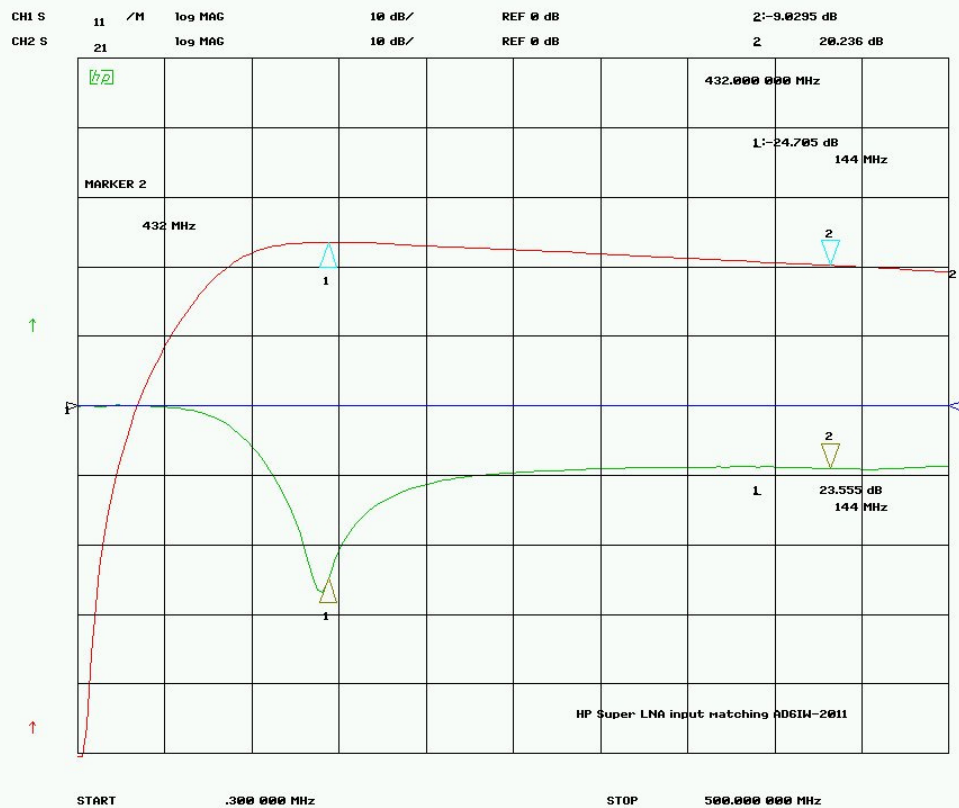




Super LNA 2M input circuit matching. Before matching IRL -6dB

Used high pass configuration, series input capacitor 22pF, inductor of 68nH

Over 0.1u, decoupling capacitor to ground.

MMIC SPF5122Z Input impedance on 144MHz is $66.8 - j54.0$ Ohms.

The NF, gain, RL before matching was 0.35dB/24.63dB/-6dB and after matching

0.59/25.5/-25dB or VSWR 3:1 before and after 1.1:1

Low pass matching was done with 3pF capacitor between input of MMIC and ground.

Series inductor of 68nH is connected between input pin and input coupling capacitor.

AD6IW 2011